



CENTRE FOR COMPUTERS & COMMUNICATION TECHNOLOGY

Result of Semester V(SUPP-I) Examination JUNE 2011

Batch 2008

COURSE: DIPLOMA IN ELECTRONICS & HARDWARE MAINTENANCE TECHNOLOGY

SEMESTER-V

Sl. No.	Roll No.	Name	THEORY															PRACTICAL									TOTAL(Theo)	TOTAL(Prac)	GRAND TOTAL	% A g e	R E M A R K											
			IE			MP-II			MEE			EP			ELECTIVE			R & PLC			IE			MP								MEE			R & PLC							
			Se (30)	Sem (70)	T (100)	Se (30)	Sem (70)	T (100)	Se (30)	Sem (70)	T (100)	Se (30)	Sem (70)	T (100)	Se (30)	Sem (70)	T (100)	Se (30)	Sem (70)	T (100)	Se (50)	Sem (50)	T (100)	Se (50)	Sem (50)	T (100)						Se (50)	Sem (50)	T (100)								
1	C08D-EH04	AMRIT CHETTRI	***	***	***	16	28	44	***	***	***	***	***	***	***	***	***	***	***	***	31	25	56	***	***	***	***	***	***	***	***	***	***	***	***	***	***	***	***	***	***	PASS

IE	INDUSTRIAL ELECTRONICS
MP-II	MICROPROCESSOR INTERFACING
MEE	MAINTENANCE OF ELECTRONIC EQUIPMENTS
EP	ELECTRONICS PACKGING
R&PLC	ROBOTIC & PLC
ELECTIVE	SAT. COMM/ MICROCONTROLLER

Theory: Maximum Pass % is 40% Se (Sessional Marks): F.M. 30 P.M. : 12 Sem (Semester Marks) : F.M.: 70 P.M. 28	Practical: Minimum Pass % 50% Se (Sessional Marks): F.M. 50 P.M. : 25 Sem (Semester Marks) : F.M.: 50 P.M. 25
WD	Result Witheld,will be anounced after clearance .
DB	Debarred from appearing Semester Exams